

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#5
12-19-02
J. Carter

In re application of

Docket No: Q57471

Yoshiyasu INOUE

Appln. No.: 09/478,198

Group Art Unit: 2879

Confirmation No.: Unknown

Examiner: Unknown

Filed: January 05, 2000

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For: METHOD OF FABRICATING A DISPLAY PANEL AND METHOD OF
RELOCATING A DISPLAY PANEL

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98**

Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and/or listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

1. Japanese Unexamined Patent Publication No. 10-41369, published February 13, 1998.
2. Japanese Unexamined Patent Publication No. 04-10454, published January 14, 1992.

One copy of each of the listed documents is submitted herewith.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date for an application other than a continued prosecution application (CPA) under §1.53(d); (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after filing a

Y. INOUE
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Information Disclosure Statement

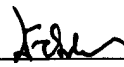
request for continued examination (RCE) under §1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant encloses herewith a copy of a Korean Office Action dated October 30, 2002 with a Japanese translation thereof. Also enclosed is an English translation of the pertinent portions of the Japanese translation which cites such documents and indicates the degree of relevance found by the foreign patent office.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

Respectfully submitted,

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WASHINGTON OFFICE



23373

PATENT TRADEMARK OFFICE

Date: December 16, 2002

Ref Q57471

[Description]

The invention as per the present application relates to a method of fabricating display panels and a method of relocating display panels which allows the fabrication rate to be increased in display panel fabrication, in particular, which allows the time required for the stage of injecting liquid crystal into the space formed between substrates to be shortened. Japanese Unexamined Patent Application Publication H10-41369 (published February 13, 1998) provides a substrate relocation device and substrate holding mechanism capable of relocating a substrate series consisting of multiple arranged substrates between a first carrier and a second carrier; furthermore, Japanese Unexamined Patent Application Publication H4-10454 (published January 14, 1992) provides a wafer relocating device which comprises a wafer separation mechanism which separates sets of two wafers and an inversion mechanism which sets the ends of even and odd numbered wafers into different positions, allowing the number of wafers exchanged to be doubled and the efficiency of the time consuming diffusion process to be increased. The inventions described in Claims 1 through 34 of the present application could have been easily invented by combining the aforementioned two cited inventions.

[Attachments]

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| Attachment 1 | Japanese Unexamined Patent Application Publication H10-41369
(Published February 13, 1998) |
| Attachment 2 | Japanese Unexamined Patent Application Publication H4-10454
(Published January 14, 1992) |